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91. The integrated circuit of claim 90, wherein the shutdown terminal is the compensation terminal.

92. The integrated circuit of claim 90, wherein the switching transistor structure is a bipolar transistor.

REMARKS

Summary of Examiner's Action

The Examiner rejected claims 1-85 as being based on a defective reissue declaration. The declaration is considered defective because it does not specify the error relied upon to add new claim 83, particularly the limitation of "the control signal is generated when the current sense signal substantially equals the error signal."

Summary of Applicant's Response

Applicant submits herewith a supplemental reissue declaration which particularly specifies the error relied upon for claim 83, as well as additional errors relied upon for new claims 86-92.

The Error Relied Upon For Claim 83

The Examiner has rejected claims 1-85 because the reissue declaration was not updated by applicant to specify the error relied upon for the language of claim 83 as amended by applicant in a response dated February 24, 1992. In paragraphs 15-18 of the supplemental reissue declaration submitted herewith, applicant particularly specifies the error relied upon for claim 83. Applicant respectfully submits that the supplemental reissue declaration overcomes the Examiner's rejection.

New Reissue Claims 86-92

Applicant has added new reissue claims 86-92 to correct errors appearing in claims 2-39 and 56-74 of the '070 patent. These claims, like the claims of the '070 patent discussed in applicants' original April 5, 1991 declaration (i.e. claims 1, 40-53 and 75-81), unnecessarily limit the claimed invention to an integrated circuit having means for operating the integrated circuit in an "isolated flyback mode" and/or means for performing a plurality of recited functions responsive to control signals applied to multi-function terminals.

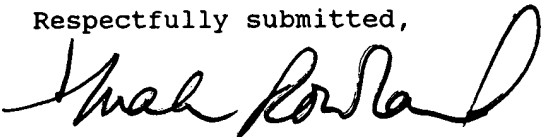
Claims 2-39 and 56-74 of the '070 patent are unlike claims 1, 40-55 and 75-81 in that they do not limit the claimed integrated circuit to "at most" five terminals. In accordance with the scope of the disclosure of the '070 patent, claims 2-39 and 56-74 allow for more than five terminals. Correction of the aforementioned errors in claims 2-39 and 56-74 will therefore provide claims of different scope than the reissue claims originally presented to correct the errors of claims 1, 40-55 and 75-81, because those reissue claims (i.e. claims 82-85) are limited to an integrated circuit having "at most" five terminals.

Applicants' supplemental reissue declaration submitted herewith particularly specifies the errors in claims 2-39 and 56-74 relied upon for new reissue claims 86-92 (see, e.g., ¶ 6 of the supplemental declaration) and how those errors arose (¶ 7) (including a statement that they arose without any deceptive intention), and acknowledges the duty to disclose all known material information pursuant to 37 C.F.R. §1.175.

Conclusion

Applicant respectfully submits that this application is in condition for allowance. Reconsideration and prompt allowance of this application are respectfully requested.

Respectfully submitted,

A handwritten signature in dark ink, appearing to read 'Mark D. Rowland', written over a horizontal line.

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